

Code No: 07A5EC07

Set No. 1

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

III B.Tech. I Sem., I Mid-Term Examinations, September – 2010

COMPUTER ORGANIZATION

Objective Exam

Name: _____ Hall Ticket No.

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Answer All Questions. All Questions Carry Equal Marks. Time: 20 Min. Marks: 20.

I Choose the correct alternative:

1. Overflow cannot occur _____ []
a. if both numbers are +ve or –ve b. if a +ve number is added to a –ve number
c. when two numbers in signed two's complement form are added
d. when two numbers in signed one's complement form are added
2. Memory is shared in _____ []
a. Parallel processing b. Multi computers c. Multi processors d. Both a and c
3. Identify the output device _____ []
a. Scanner b. Keyboard c. Joy stick d. Plotter
4. Intermediate results are stored in _____ []
a. Data Register b. Accumulator c. Temporary Register d. Program Counter
5. Instruction cycle is equal to _____ []
a. Fetch cycle + Decode Cycle b. Fetch cycle + Execute Cycle
c. Decode cycle + Execute cycle d. Fetch cycle + Decode cycle + Execute cycle
6. Address of next instruction is stored in _____ []
a. Program counter b. Instruction register c. Accumulator d. Data Register
7. Complement of mask operation is _____ []
a. XOR b. Selective Complement c. Selective clear d. Selective set
8. $X = (A+B) * (C+D)$ is a _____ []
a. One address instruction b. Two address instruction
c. Three address instruction d. Zero address instruction
9. Single cycle instruction execution is found in _____ []
a. CISC b. ARM c. RISC d. none
10. Postfix notation of $(A+B)*C$ is _____ []
a. $AB+*C$ b. $AB*+C$ c. $ABC+*$ d. $AB+C*$

Cont.....2

II Fill in the blanks:

11. Number of hits divided by number of memory references is called _____
12. PSW stands for _____
13. If 2 inputs of JK flip flop are connected together then it is called _____
14. _____ code is used for error correction
15. A floating point number is said to be _____ if most significant bit is non zero digit
16. In micro programmed control the control information is stored in _____
17. SHRA instruction stands for _____
18. _____ represents effective address offset with in a segment
19. _____ algorithm is used for multiplying signed 2's complement numbers
20. _____ is hardware implementation of branch and save return address Operation.

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