JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
II B.Tech. II Sem., II Mid-Term Examinations, April/May – 2010
COMPUTER ORGANIZATION
Objective Exam

Name: _____ Hall Ticket No. ☐☐☐☐☐ **A** ☐☐☐☐

Answer All Questions. All Questions Carry Equal Marks.Time: 20 Min. Marks: 20.

I   Choose the correct alternative:

1.   The cells in each column are connected to a sense/ write circuit by _____ bit lines   [   ]
        a) 1   b) 2   c) 3   d)   4

2.   Transfer of data under --- is between cpu & peripheral   [   ]
        a) programmed I/O   b) Interrupt Initiated I/O   c) DMA   d) none

3.   Data transfer modes are _____   [   ]
        a) programmed I/O   b)Interrupt Initiated I/O   c) DMA   d) none

4.   The command field corresponds to an operation code that specifies ___ basic types of I/O operation .
                                                                                        [   ]
        a) 2   b) 4   c)   6   d) 8

5.   The operation s performed on the data in the processor constitutes a _____ stream   [   ]
        a) Instruction   b) data   c) both   d)none

6.   Flynn's classification   divides computer into ___ major groups   [   ]
        a) 1   b)2   c) 3   d) 4

7.   Multi processors are classified into _____ types.   [   ]
        a) tightly coupled   b) loosely coupled   c) both   d) none

8.   A measure used to evaluate computer in their ability to perform a given number of floating –point
        operations per second is referred as -----   [   ]
        a) byte   b) bit   c) flops   d) none

9.   The packet consists of _____   [   ]
         a) address   b) context   c) error detection code   d) all

10.   In UART "R" means _____   [   ]
         a) radio   b) receiver   c) repeat   d) none

II   Fill in the blanks:

11.   The mapping function is implemented by a special memory Control circuit , often called the _____ circuit.

12.   Expand RAS _____

13.   Stored data are organized on CDROM tracks in the form of blocks that are called _____

14.   Expand   DVD _____

15.   Instruction that are read from memory by an IOP are sometimes called _____

16.   The time required to move the read/write head to the proper track is called _____ time

17.   The cpu communicates with IOP by enabling the   _____ line

18.   In a _____ coupled multiprocessor system, the source is a processor and the destination is a memory module.

19.   The characters that control the transmission are called _____ characters.

20.   _____ is used to refer to the amount of time it takes to transfer a word of data to or from the memory.

-oOo-

Code No: 07A4EC13   **Set No. 2**

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                                                                                                                       [   ]
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        operations per second is referred as _____   [point   ]
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6.   The packet consists of _____   [   ]
         a) address   b) context   c) error detection code   d) all

7.   In UART "R" means _____   [   ]
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8.   The cells in each column are connected to a sense/ write circuit by _____ bit lines   [   ]
        a) 1   b) 2   c) 3   d)   4

9.   Transfer of data under ___ is between cpu & peripheral   [   ]
        a)programmed I/O   b)Interrupt Initiated I/O   c) DMA   d) none

10.   Data transfer modes are _____   [   ]
        a)programmed I/O   b)Interrupt Initiated I/O   c) DMA   d) none

                                                                                                        Cont...2

II   Fill in the blanks:

11.   Expand DVD _____

12.   Instruction that are read from memory by an IOP are sometimes called _____

13.   The time required to move the read/write head to the proper track is called _____ time

14.   The cpu communicates with IOP by enabling the   _____ line

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Cont...2

II   Fill in the blanks:

11.   The time required to move the read/write head to the proper track is called _____ time

12.   The cpu communicates with IOP by enabling the _____ line

13.   In a _____ coupled multiprocessor system, the source is a processor and the destination is a memory module.

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